

Figure 1 : Router Architecture

CIS-032-B

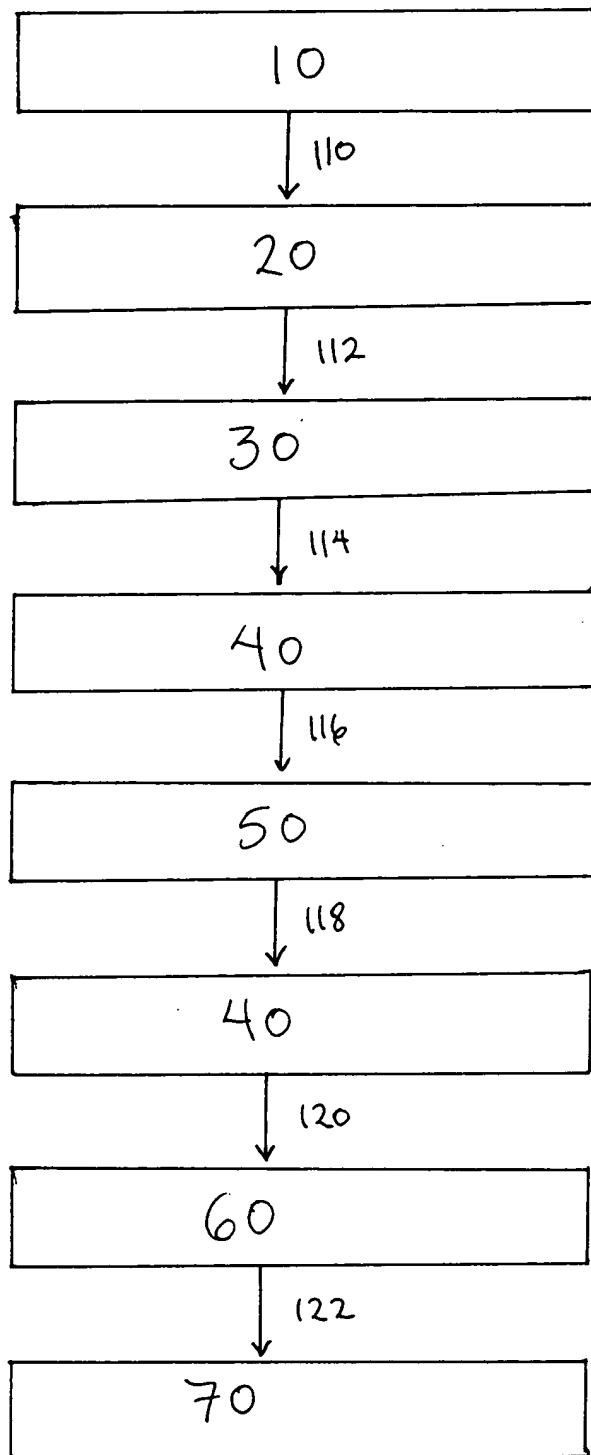


Figure 2: Sample Flow Diagram for a Data Packet

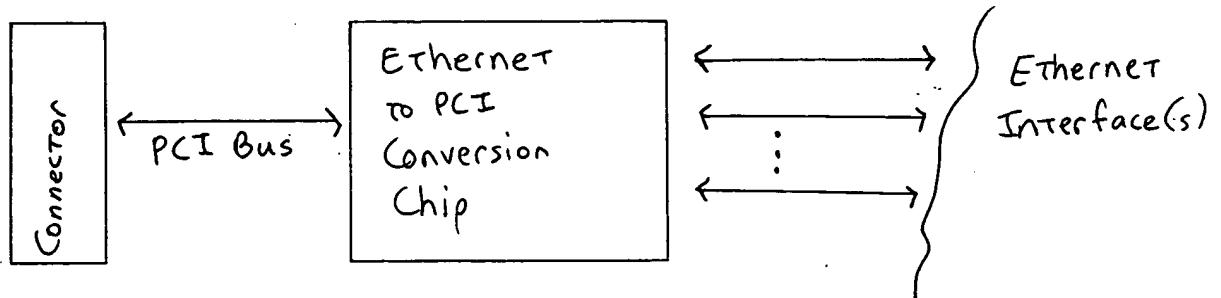


Figure 3a: Port Adaptor Design for an Ethernet Interface

SECTION 3: DESIGN

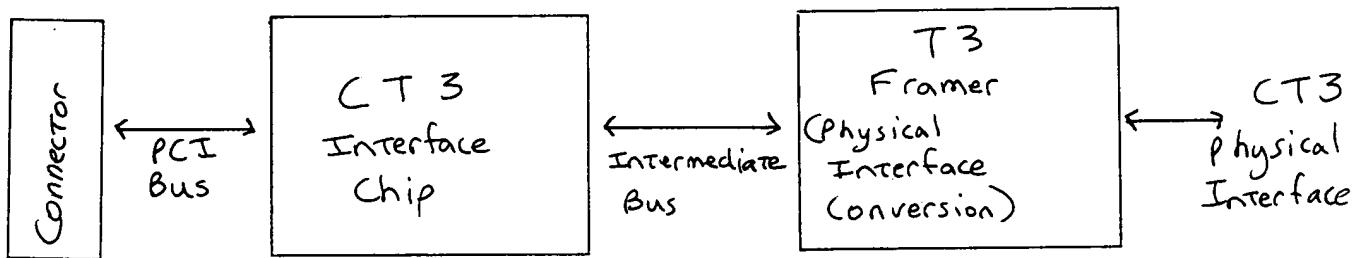


Figure 3b: Port Adaptor Design for a Channelized T3 Line (CT3) Interface

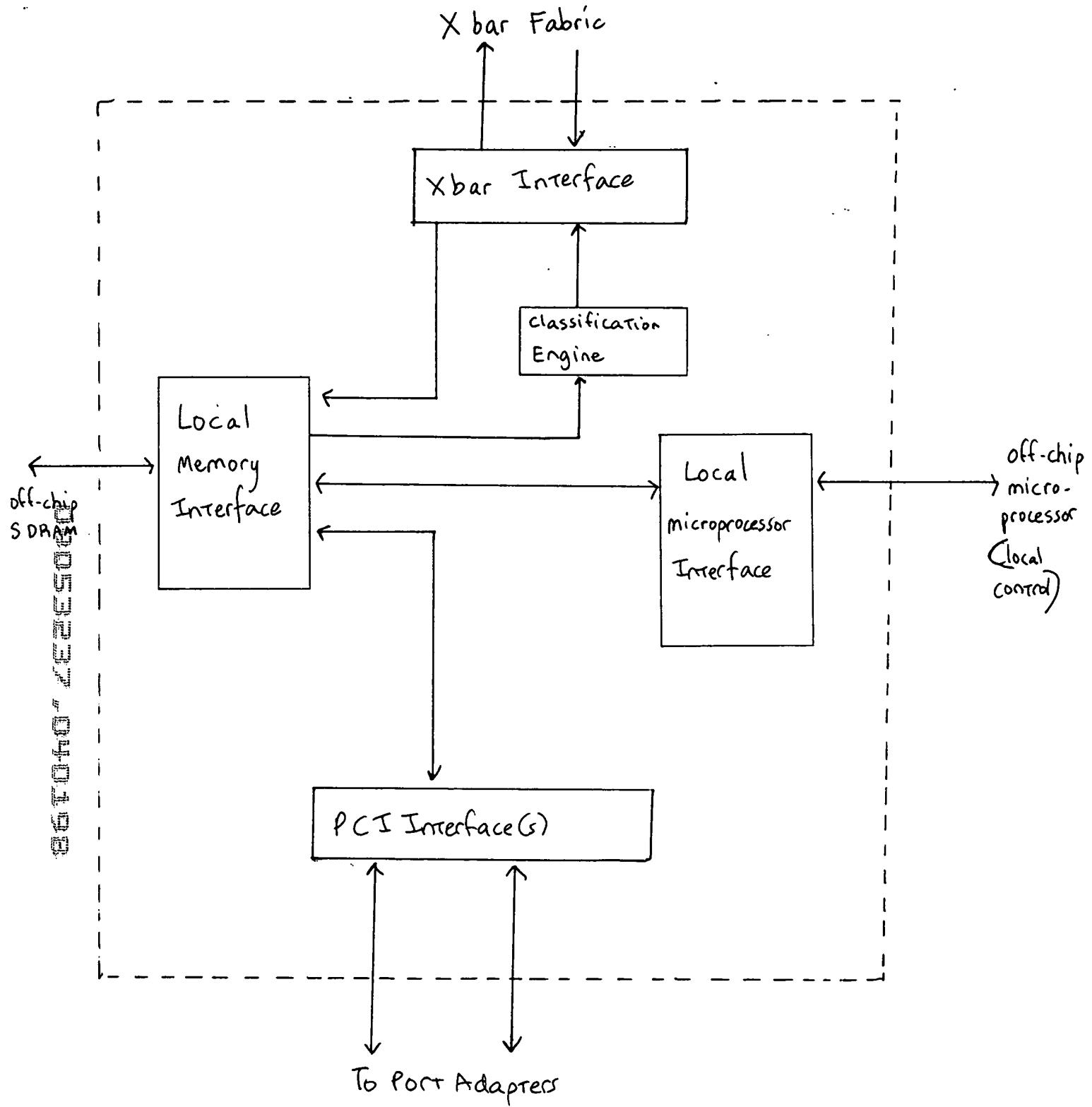


Figure 4: Data Compiler